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Introduction to VLSI CMOS Circuits Design 1 Carlos Silva Cardenas Catholic University of PeruÂ´ Takeo Yoshida University of the Ryukyus Alberto Palacios Pawlovsky Toin University of Yokohama August 18, 2006 1Work supported by a grant of the Ministry of Education and Science of Japan and the Toin University of Yokohama.

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His research interests include CMOS VLSI design, microprocessors, and computer arithmetic. He holds a dozen patents, is the author of three other books in the field of digital design and three hiking guidebooks, and has designed chips at Sun Microsystems, Intel, Hewlett-Packard, and Evans & Sutherland.

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